

### **SSD** Adoption: When, Where, How?

### Doreet Oren SSD Director Product Marketing SanDisk

Santa Clara, CA USA August 2008



- SSD adoption: drivers and barriers
- Introducing the Netbook (ULCPC)
- Vertical integration a key
- Summary conclusions



### SSD adoption drivers:

- Ruggedness, performance, power consumption, form factor, heat, noise
- What about cost and capacity?
- Are these barriers today?



### **Introducing the Netbook**



Adopted as new "must have" device
Primary purpose - surfing the Web



Three important benefits explain why this new market is so compelling:

- Convenient web connectivity
- Easy mobility
- Low cost









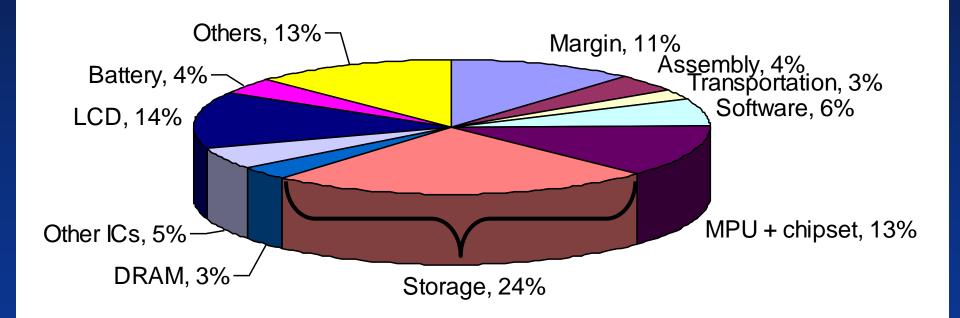


# Today's Typical Netbook Specifications

| Retail price                                                                                                                                                | \$250-\$600                         |  |  |  |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------|--|--|--|
| Weight                                                                                                                                                      | ~2lbs                               |  |  |  |
| LCD panel                                                                                                                                                   | 7"-9"                               |  |  |  |
| Storage type & capacity(*)                                                                                                                                  | Flash-based solid state drive (SSD) |  |  |  |
|                                                                                                                                                             | 4GB-16GB                            |  |  |  |
| Microprocessor                                                                                                                                              | Intel Celeron-M, Atom, VIA C7-M     |  |  |  |
| Operating system                                                                                                                                            | Linux, Microsoft XP                 |  |  |  |
| Memory card slot                                                                                                                                            | mory card slot 1-2                  |  |  |  |
| Wireless Connection (**)                                                                                                                                    | 802.11b/g                           |  |  |  |
| <ul><li>(*) HDD options are available for higher capacity</li><li>(**) Some manufacturers are partnering with service providers for 3G web access</li></ul> |                                     |  |  |  |



### Netbook Components Relative Cost



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Source: DAIWA Institute of Research 2008



# **Netbook PCB - HDD or SSD?**





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## SSD Value for Netbooks Endorsed by Analysts and PC OEMs

*"From a storage perspective, ULCPCs and SSDs appear to be a good match."* 

SSDs provide the required:

- •Small form factor
- •Durability
- •Low power consumption

...perhaps most importantly, SSDs can provide the **required capacity at very low price points.**"

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IDC, "Ultra Low Cost PCs and SSDs. A Good Match?", Jeff Janukowicz, IDC #212765, June 2008



#### PRESS RELEASE

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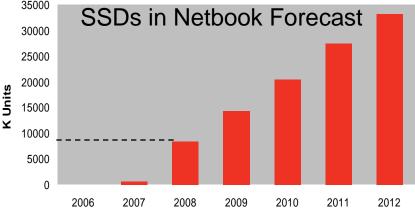
#### SANDISK LAUNCHES SOLID-STATE DRIVES AIMED AT HOT NEW CATEGORY OF ULTRA LOW-COST PCs <u>SanDisk pSSD Flash Memory Modules Are Positioned As Storage</u> of Choice For Emerging "Must-Have" Market of ULCPCs or 'Netbooks'

#### TAIPEI, TAIWAN AND MILPITAS, CALIFORNIA, June 3, 2008 -

According to Joseph Unsworth, Research Director at Gartner, The **opportunity for SSDs in the emerging market of ultra low-cost PCs is promising.**" Gartner expects the low-cost SSD category to grow from 635,000 units in 2007 to **over 33 million units in 2012**,

and that represents a five-year compound growth rate of 117 percent.3 "As semiconductor innovation enables more powerful functionality at lower prices, storage requirements will continue to be elastic,

providing opportunities for companies that can command compelling low-cost SSD solutions," said Unsworth.



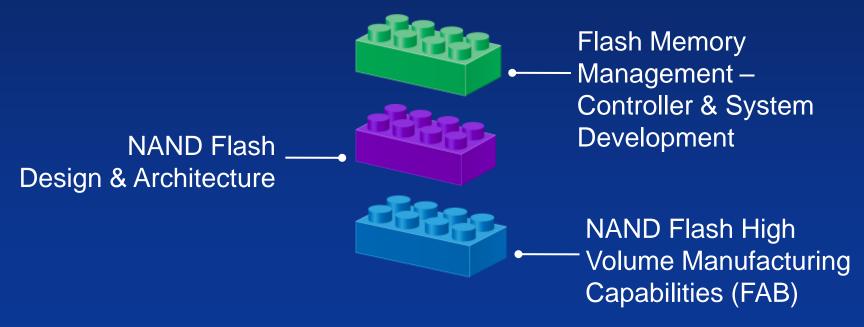
SanDisk Corporation 601 McCarthy Boulevard Milpitas, CA 95035-7932 Phone: 408-801-1000





### What Does it Take From Flash Vendors to Make SSD Adoption a Reality?

### Vertical Integration Across Major Building Blocks



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# **SSD Market Requires Assurance of Memory:**



Availability – Petabytes of NAND for multi \$B market
 Stable competitive price – economy of scale will drive consolidation

 Quality – vertical integration for internal feedback loop

Petabyte =  $2^{50}$  bytes

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SanDisk-Toshiba 300mm Fabs Yokkaichi, Japan



# NAND Technology Roadmap





|          |      | 2006 | 2007                         | 2008                     | 2009         | 2010 |
|----------|------|------|------------------------------|--------------------------|--------------|------|
| Capacity | 128G |      |                              |                          |              |      |
|          | 96G  |      | orocess shrin                | k                        |              |      |
|          | 64G  |      | y doubling<br>ts per cell (M | LC)                      | 43nm<br>X4   | 3Xnm |
|          | 32G  |      |                              |                          | Snm 3X<br>X3 | nm   |
|          | 16G  |      |                              | 43nm<br>X2<br>56nm<br>X3 |              |      |
|          | 8G   |      | õnm<br>X2                    |                          |              |      |
| Santa    | 4G   |      |                              |                          |              |      |



### **Not All MLC Is Created Equal**



### New vendors just entering MLC market

### MLC history:

- 1992 First MLC Patent Eli Harari
- SanDisk-Toshiba Partnership
  - 2001 First NAND MLC Product
  - 2002-2008 6 generations of MLC

| Santa C | lara, | CA | USA |
|---------|-------|----|-----|
| August  | 2008  |    |     |

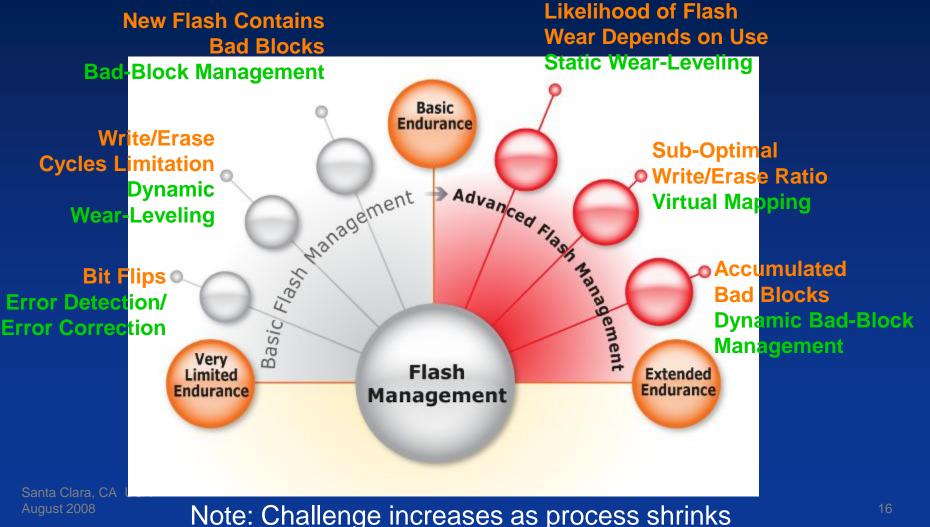
| United States Patent [19]<br>Harari                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | US00099344A<br>[11] Patent Number: 5,095,344<br>[45] Date of Patent: Mar. 10, 1992                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| [54]         HIGHLY COMPACT EPROM AND FLASH<br>EEPROM DEVICES           [76]         Inventor:         Ellpahou Harari, 2320 Friars La.,<br>Los Altos, Calif. 94012           [21]         Appl. No.:         214,175           [22]         Filed:         Jun. 8, 1988           [51]         Int. Cl <sup>3</sup> H01L 29/78; H01L 27/01;<br>H01L 29/70; H01L 29/70; H01L 27/04;<br>357/23.5; 357/23, 457/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 557/23, 577/23, 577/23, 577/23, 577/23, 577/23, 577/23, 577/23, 577                                                                                                                                                                                                                                                                                                                                                                       | of the IEEE International Electron Device Meeting, Dec.<br>1984, pp. 480-483.<br>Y. Mintani and K. Makita, "A New EPROM Cell with<br>a Side-Wall Floating Gate for High-Decesity and High-<br>Performance Device", 1985 IEDM Technical Diges,<br>pp. 635-638.<br>F. Masocka et al., "A 256K Flash EEPROM Using<br>Triple Polyaeilcon Technology", Digest of Technical<br>Papers, IEEE International Solid-State Circuit: Confer-<br>ence, Feb. 1985, pp. 168-169, p. 335.<br>A. T. Wa et al., "A Novel High-Speed, 5-Volt Pro-<br>gramming EPROM Structure with Source-Side Injec-<br>tion", 1986 IEDM Technical Digest, pp. 584-587.<br>G. Samachins et al., "A 128K Flash EEPROM Using<br>Deable-Polyaeilicon Technology", IEEE Journal of |
| U.S. PATENT DOCUMENTS<br>4,531,948 5/1942 Geney, Jr. et al                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | Solid Sate Circuit, Oct. 1987, vol. SC-22, No. 5, pp.<br>676-683.<br>(List continued on next page.)<br>Primary Examiner—Andrew J. James<br>Ansituse Examiner—Datiel Kim                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| 4,485,769 12/1984 Sinko 377,23 5<br>4,503,519 3/1985 Anikawa 357,23 5<br>4,577,21 5 3/1985 Anikawa 357,23 5<br>4,655,417 5/1987 Lam 377,23 5<br>4,717,943 1/1988 Hazmi 366/51<br>4,794,595 12/1988 Hazmi 366/51<br>4,794,595 12/1988 Woll et al 357,23 5<br>4,813,529 2/1989 Baker et al 357,23 5<br>4,832,062 7/1989 Baker et al 357,23 5<br>5,832,062 7/1989 Baker et al 357,23 5<br>5,832,062 7<br>5,832,062 7<br>5 | Altorney, Agent, or First-Majestic, Parsons, Siebert &<br>Hote<br>[57] - ABSTRACT<br>Structures, methods of manufacturing and methods of<br>use of electrically programmable read only memories<br>(EPROM) and flash electrically ensable and program-<br>mable read only memories (EEPROM) include split<br>channel and other cell configurations. An arrangement                                                                                                                                                                                                                                                                                                                                                                           |
| FOREIGN PATENT DOCUMENTS<br>0047133 3/1962 European Pat. Off                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | of elements and cooperative processes of manufacture<br>provide self-alignment of the elements. An intelligent<br>programming technique allows each memory cell to<br>store more than the usual use bit of information. An<br>intelligent erase algorithm prolongs the useful life of the<br>memory cells. Use of these various features provides a<br>memory having a very high storage density and a long<br>He, making it particularly useful as a tolid state memory<br>in place of magnetic disk storage devices in computer                                                                                                                                                                                                            |

#### S. Tanaka et al., "A Programmable 256K CMOS

ble 256K CMOS systems.



## Flash Management **Know-How Required**

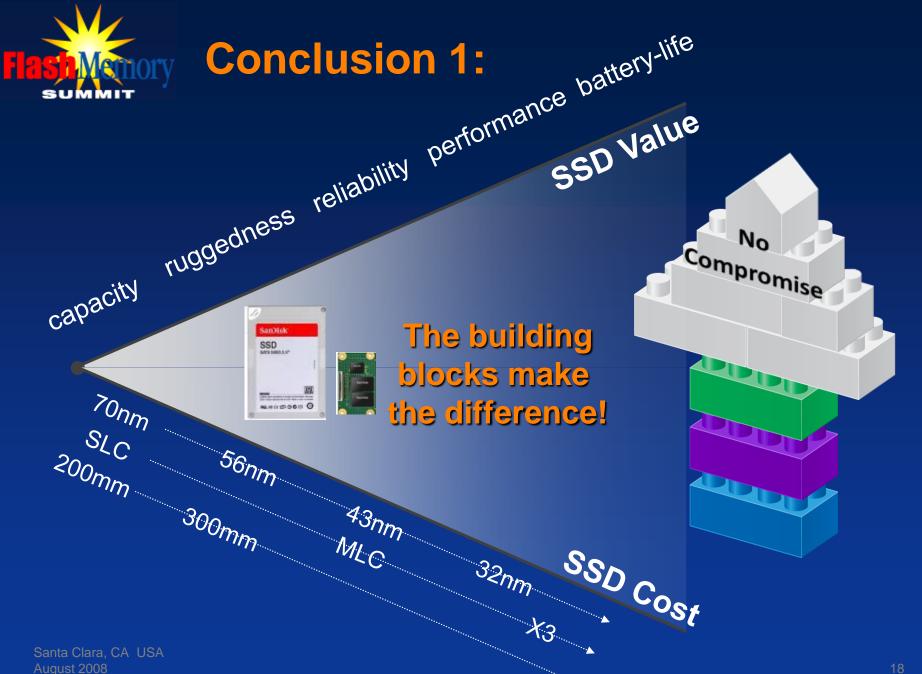


Controller &

System Dev.



### Summary: SSD Adoption Where When How?





### **Conclusion 2:**

### SSD value answers Netbook needs today



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SanDisk Know-how onboard









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